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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al.

Art Unit : Unknown

Serial No. : 10/007,007

Examiner : Unknown

Filed : December 4, 2001

Title : PROVIDING MODELING INSTRUMENTATION WITH AN APPLICATION  
PROGRAMMING INTERFACE TO A GUI APPLICATION

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SEP 16 2003

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

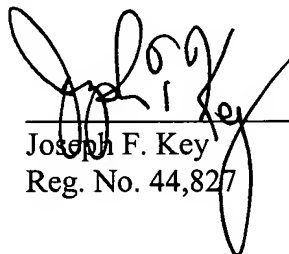
Copies of the references listed on the attached form PTO-1449 are enclosed.

This statement is being filed before the receipt of a first Office action on the merits.

Please apply any charges or credits to Deposit Account No. 06-1050.

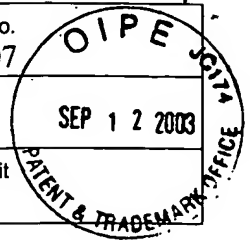
Respectfully submitted,

Date: 9/12/2003

  
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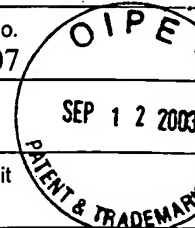
Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-606001	Application No. 10/007,007
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	
		Filing Date December 4, 2001	Group Art Unit



### U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,598,347	01/28/1997	Iwasaki			
	AB	RE 38,059	04/01/2003	Yano et al.			
	AC	6,574,787	06/03/2003	Anderson			
	AD	6,539,536	03/25/2003	Singh et al.			
	AE	6,523,156	02/18/2003	Cirit			
	AF	6,477,688	11/05/2002	Wallace			
	AG	6,440,780	08/27/2002	Kimura et al.			
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	AT	5,724,250	03/03/1998	Kerzman et al.			
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	AY	5,287,289	02/15/1994	Kageyama et al.			
	AZ	4,703,435	10/27/1987	Darringer et al.			
	AAA						

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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		Applicant William R. Wheeler et al.	
		Filing Date December 4, 2001	Group Art Unit
			

### Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	ABB	JP 2001068994	03/16/2001	Japan	RECEIVED		Abstract only	
	ACC	EP 720233	07/03/1996	European Patent	SEP 16	2003	X	
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	AEE	JP 58060559	04/11/1983	Japan			Abstract only	
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### Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AGG	NN8006341, "Macro Physical-To-Logical Checking LSI Chip Design", IBM Technical Disclosure Bulletin, Vol. 23, No. 1, June 1980, pp. 341-345 (7pages)
	AHH	Kutzschebauch, "Efficient logic optimization using regularity extraction", Proceedings of 2000 International Conference on Computer Design, 17 September 2000, pp. 487-493.
	AII	
	AJJ	

Examiner Signature	Date Considered
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